TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM8GZ47,SM8JZ47,SM8GZ47A,SM8JZ47A

AC POWER CONTROL APPLICATIONS

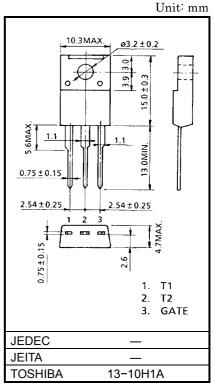
Repetitive Peak Off-State Voltage : VDRM = 400, 600V
 R.M.S ON-State Current : IT (RMS) = 8A

• High Commutating (dv / dt)

• Isolation Voltage : $V_{\rm ISOL} = 1500 \rm V AC$

MAXIMUM RATINGS

CHARACTERI	STIC	SYMBOL	RATING	UNIT	
Repetitive Peak	SM8GZ47 SM8GZ47A	Vanu	400	٧	
Off-State Voltage	SM8JZ47 SM8JZ47A	V_{DRM}	600		
R.M.S On-State Current (Full Sine Waveform Tc	•	I _{T (RMS)}	8	А	
Peak One Cycle Surge On-State		I _{TSM}	80 (50Hz)	А	
Current (Non-Repetitive)	TSM	88 (60Hz)	A	
I ² t Limit Value		I ² t	32	A ² s	
Critical Rate of Rise of C Current	On-State (Note 1)	di / dt	50	A / μs	
Peak Gate Power Dissip	ation	P_{GM}	5	W	
Average Gate Power Dis	ssipation	P _{G (AV)}	0.5	W	
Peak Gate Voltage		V_{GM}	10	V	
Peak Gate Current		I _{GM}	2	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Ra	ange	T _{stg}	-40~125	°C	
Isolation Voltage (AC, t =	= 1min.)	V _{ISOL}	1500	٧	



Weight: 1.7g

Note 1: di / dt Test Condition $V_{DRM} = 0.5 \times Rated$

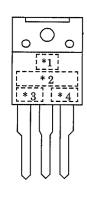
 $I_{TM} \le 12A$ $t_{gw} \ge 10 \mu s$ $t_{gr} \le 250 ns$ $i_{GP} = I_{GT} \times 2.0$



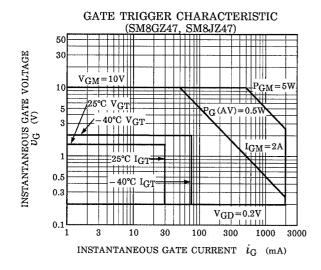
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

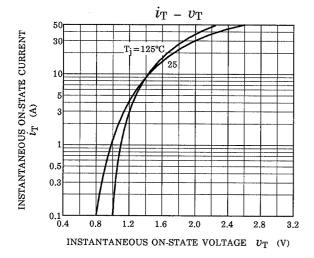
CHAF	RACTERIS	TIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak (Current	Off-State			I _{DRM}	V _{DRM} = Rated		_	_	20	μА
	Gate Trigger Voltage I		I			T2 (+), Gate (+)	_	_	1.5	
Cata Trigger Val			_	_	1.5	V				
Gate Trigger Voltage		III	VGT	$R_L = 20\Omega$	T2 (-), Gate (-)	_	_	1.5	1 V	
			IV		T2 (-), Gate (+)		_	_	_	
		SM8GZ47 SM8JZ47				T2 (+), Gate (+)	_	_	30	-
						T2 (+), Gate (-)	_	_	30	
SI	SM8JZ4					T2 (-), Gate (-)	_	_	30	
Gate Trigger			IV	,	V _D = 12V	T2 (-), Gate (+)	_	_	_	mA
			I	I _{GT}	$R_L = 20\Omega$	T2 (+), Gate (+)	_	_	20	
	SM8GZ	SM8GZ47A SM8JZ47A	II			T2 (+), Gate (-)	_	_	20	
	SM8JZ4		III			T2 (-), Gate (-)	_	_	20	
						T2 (-), Gate (+)	_	_	_	
Peak On-State Voltage		V _{TM}	I _{TM} = 12A		_	_	1.5	V		
Gate Non-Trigge	er Voltage			V _{GD}	V _D = Rated, Tc = 125°C		0.2	_	_	V
Holding Current	olding Current I_H $V_D = 12V$, $I_{TM} = 1A$		_	_	50	mA				
Thermal Resistar	mal Resistance R _{th (j-c)} Junction to Case, AC		se, AC	_	_	3.6	°C/W			
		SM80 SM8J		dv / dt	dv / dt V _{DRM} = Rated, T _j = 125°C		_	300	_	- V / μs
Voltage			GZ47A IZ47A	dv / dt	Exponential Ris	Exponential Rise		200	_	ν / μ3
Critical Rate of Rise of Off–State		SM80 SM8J		(dv / dt) c	V _{DRM} = 400V, T _i = 125°C		10		_	· V/μs
Voltage at Commutation		SM80 SM8J	GZ47A IZ47A	(uv / ut) C	(di/dt) c = -4.5A/ms		4	_	_	ν / μ5

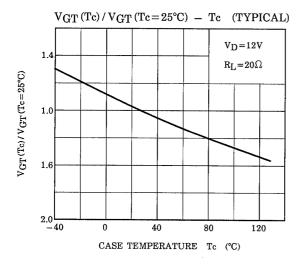
MARKING

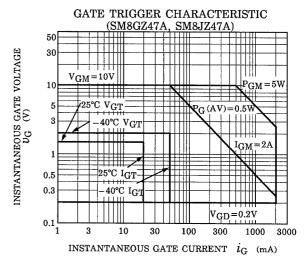


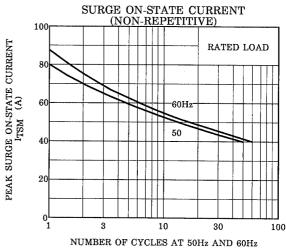
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* NUMBER		SYMBOL	MARK	
* 1	TOSHIBA PRODUCT MARK		7	
* 2		SM8GZ47, SM8GZ47A	M8GZ47	
	TYPE	SM8JZ47, SM8JZ47A	M8JZ47	
* 3		SM8GZ47A, SM8JZ47A	Α	
* 4	Lot Number Month(Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)		Example 8A : January 1998 8B : Febrary 1998 8L : December 1998	

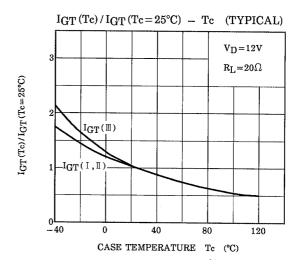


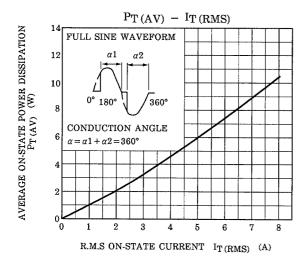


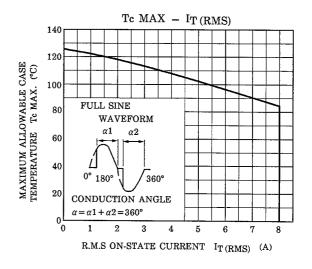


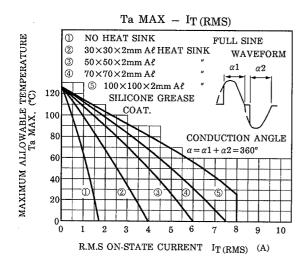


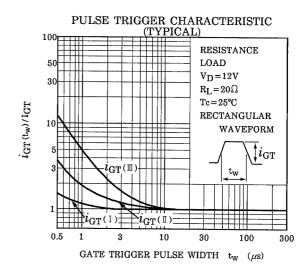


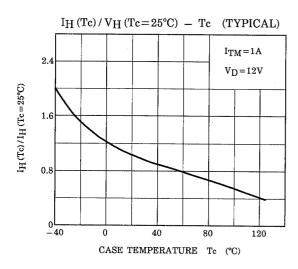


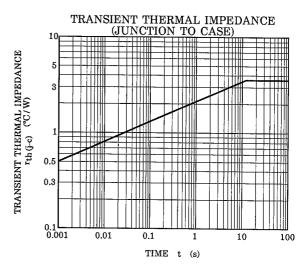












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