

M494

## SINGLE-CHIP VOLTAGE TUNING SYSTEM WITH 4 ANALOG CONTROLS AND $\mu P$ INTERFACE

- NON-VOLATILE MEMORY FOR 20 PRO-GRAM WORDS (17 BIT x 20)
  - TUNING VOLTAGE 12 BITS
  - BAND 2 BITS
  - MULTI STANDARD 2 BITS
  - PROGRAM SKIP BIT 1 BIT
  - 10,000 MODIFY CYCLES PER WORD
  - MIN. 10 YEARS DATA RETENTION
- 13 BIT VOLTAGE SYNTHESIZER (BRM + PWM)
- NV MEMORY FOR 4 ANALOG CONTROLS (6 BIT x 4)
- 4 BAND SWITCH OUTPUTS (VHF I & III, UHF, CATV)
- 5 x 7 KFYBOARD

pable of including a floating gate NV memory cell (EEPROM).

The i.c. has been designed as a complete digital TV tuning system based on the voltage synthesis principle and as a replacement for all the conventional potentiometers and band switches particularly in low cost TV sets. It also provides some functions normally only associated with higher cost sets. NV memory is integrated on the chip together with all the necessary control circuitry to provide the program memory. Separate NV memory is also integrated to provide the memory for four analog controls. A seven segment LED display can be directly driven by the chip to display the program