MAC15M, MAC15N TRIAC – 400V - 800V







Features

Description

- Blocking Voltage to 800 Volts
- On-State Current Rating of 15 Amperes RMS at 80°C
- Uniform Gate Trigger Currents in Three Modes
- High Immunity to dv/dt 250 V/µs minimum at 125°C
- Minimizes Snubber Networks for Protection
- Industry Standard TO-220 Package
- High Commutating di/dt 9.0 A/ms minimum at 125°C
- Operational in Three Quadrants, Q1, Q2, and Q3
- These Devices are Pb-Free and are RoHS Compliant

Additional Information







Accessories



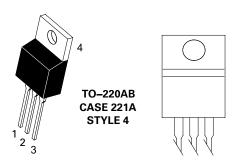
Samples

Functional Diagram



Designed for high performance full-wave AC control applications where high noise immunity and high commutating di/dt are

Pin Out





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Maximum Ratings (TJ = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Peak Repetitive Off-State Voltage (Note 1) MAC15M (Gate Open, Sine Wave 50 to 60 Hz, $T_J = -40^{\circ}$ to 125°C) MAC15N		V _{DRM} , V _{RRM}	600 800	V
On-State RMS Current (Full Cycle Sine Wave, 60 Hz, $T_c = 8$	I _{T (RMS)}	15	А	
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, T_c = 125°C)	I _{TSM}	150	А	
Circuit Fusing Consideration (t = 8.3 ms)	l²t	93	A ² sec	
Peak Gate Power ($T_c = 80^{\circ}$ C, Pulse Width <= 1.0 µs)	P_{GM}	20	W	
Average Gate Power (t = 8.3 ms, T_c = 80°C)	$P_{G(AV)}$	0.5	W	
Operating Junction Temperature Range	T	-40 to +125	°C	
Storage Temperature Range	T _{stg}	-40 to +150	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the

Thermal Characteristics

Rating		Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (AC) Junction-to-Ambient		R _{ejc} R _{eja}	2.0 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		T _L	260	°C

Electrical Characteristics - OFF (TJ = 25°C unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Repetitive Blocking Current	T ₁ = 25°C	I _{DRM} ,	-	-	0.01	mΛ
$(V_D = V_{DRM} = V_{RRM}; Gate Open)$	$T_{J}^{\circ} = 125^{\circ}C$	I _{RRM}	-	-	2.0	mA

Electrical Characteristics - ON (TJ = 25°C unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak On-State Voltage (Note 2) (I _{TM} = ±21 A Peak)		V_{TM}	-	1.2	1.6	V
Gate Trigger Current	MT2(+), G(+)		5.0	13	35	
(Continuous dc)	MT2(+), G(-)	I _{GT}	5.0	16	35	mA
$(V_{D} = 12 \text{ V}, R_{L} = 100 \Omega)$	MT2(-), G(-)		5.0	18	35	
Gate Trigger Voltage (Continuous dc)	MT2(+), G(+)	V _{GT}	0.5	0.75	1.5	
	MT2(+), G(-)		0.5	0.72	1.5	V
$(V_{D} = 12 \text{ V}, R_{L} = 100 \Omega)$	MT2(-), G(-)		0.5	0.82	1.5	
	MT2(+), G(+)	$V_{\sf GD}$	_	33	50	
Latching Current (V _D = 24 V, I _G = 35 mA)	MT2(+), G(-)		_	36	80	V
(v _D = 24 v, i _G = 33 iii/)	MT2(-), G(-)		_	33	50	
Holding Current ($V_D = 12 V_{dol}$ Gate Open, Initiating Current = ±150 mA))		I _H	_	20	40	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different

2. Indicates Pulse Test: Pulse Width ≤ 2.0 ms, Duty Cycle ≤ 2%



Recommended Operating Conditions may affect device reliability.

1. V_{DBM} and V_{SBM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

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Dynamic Characteristics

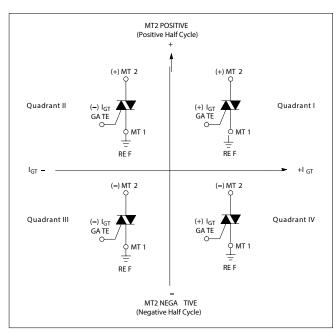
Characteristic	Symbol	Min	Тур	Max	Unit
Rate of Change of Commutating Current; See Figure 10. ($V_D = 400 \text{ V}$, $I_{TM} = 6.0 \text{ A}$, Commutating dv/dt = 24 V/ μ s, $C_L = 10 \mu$ F Gate Open, TJ = 125°C, f = 250 Hz, No Snubber) $L_I = 40 \text{ mH}$	(di/dt) _c	9.0	-	_	A/ms
Critical Rate of Rise of Off-State Voltage ($V_D = Rated V_{DRM}$, Exponential Waveform, Gate Open, TJ = 125°C)	dV/dt	250	-	_	V/µs

Voltage Current Characteristic of SCR

Symbol	Parameter
V_{DRM}	Peak Repetitive Forward Off State Voltage
l _{DRM}	Peak Forward Blocking Current
V_{RRM}	Peak Repetitive Reverse Off State Voltage
I _{RRM}	Peak Reverse Blocking Current
V_{TM}	Maximum On State Voltage
I	Holding Current

On stat e V_{TM} Quadrant 1 Main Terminal 2 + V_{TM} Off stat e V_{TM} V_{TM} Off stat e V_{DRM} Quadrant 3 Main Terminal 2 -

Quadrant Definitions for a Triac



All polarities are referenced to MT1. With in–phase signals (using standard AC lines) quadrants I and III are used

Figure 1. RMS Current Derating

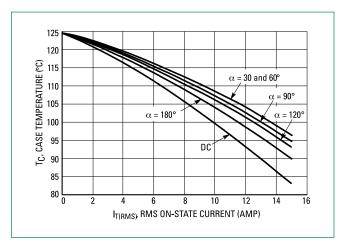


Figure 3. On-State Characteristics

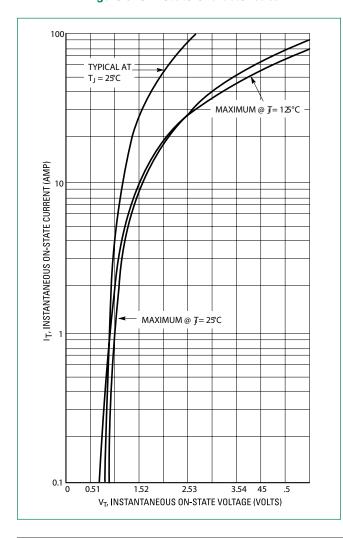


Figure 2. On-State Power Dissipation

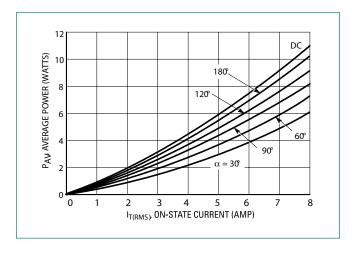


Figure 4. Thermal Response

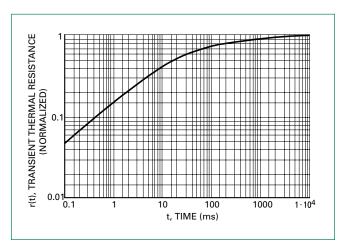


Figure 5. Hold Current Variation

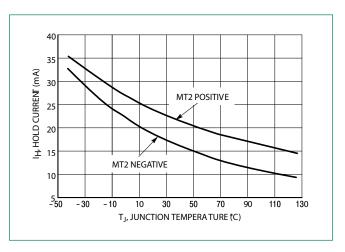




Figure 6. Gate Trigger Current Variation

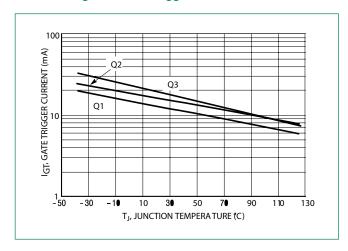


Figure 8. Critical Rate of Rise of Off-State Voltage (Exponential)

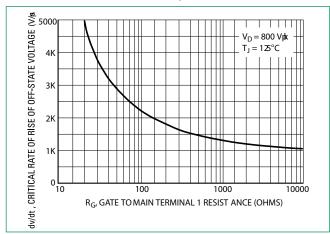


Figure 7. Gate Trigger Voltage Variation

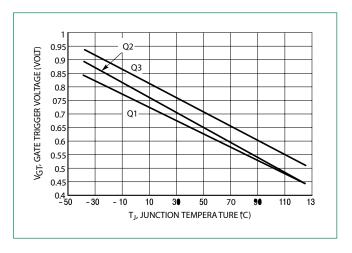


Figure 9. Critical Rate of Rise of Commutating Voltage

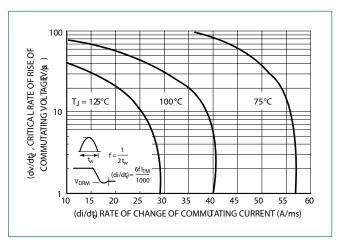
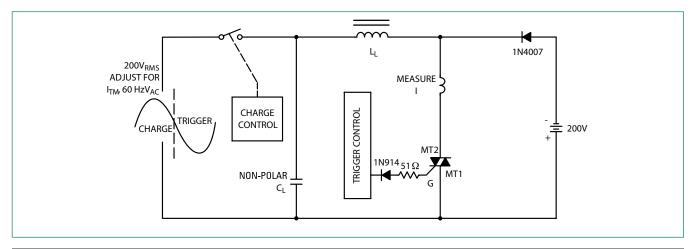


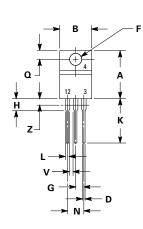
Figure 10. Simplified Test Circuit to Measure the Critical Rate of Rise of Commutating Current (di/dt)

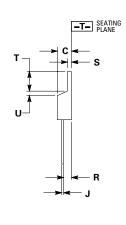




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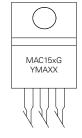
Dimensions





Part Marking System





Y =Year M =Month A =Assembly Site XX =Lot Serial Code G =Pb-Free Package

D:	Inches		Millimeters			
Dim	Min	Max	Min	Max		
Α	0.590	0.620	14.99	15.75		
В	0.380	0.420	9.65	10.67		
С	0.178	0.188	4.52	4.78		
D	0.025	0.035	0.64	0.89		
F	0.142	0.147	3.61	3.73		
G	0.095	0.105	2.41	2.67		
Н	0.110	0.130	2.79	3.30		
J	0.018	0.024	0.46	0.61		
K	0.540	0.575	13.72	14.61		
L	0.060	0.075	1.52	1.91		
N	0.195	0.205	4.95	5.21		
Q	0.105	0.115	2.67	2.92		
R	0.085	0.095	2.16	2.41		
S	0.045	0.060	1.14	1.52		
Т	0.235	0.255	5.97	6.47		
U	0.000	0.050	0.00	1.27		
V	0.045		1.15			
Z		0.080		2.04		

1	Main Terminal 1
2	Main Terminal 2
3	Gate
4	Main Terminal 2

Pin Assignment

Ordering Information

Device	Package	Shipping
MAC15MG	TO-220AB	1000 Unito / Day
MAC15NG	(Pb-Free)	1000 Units / Box

- 1. Dimensioning And Tolerancing Per Ansi Y14.5m, 1982.
- Controlling Dimension: Inch.
 Dimension Z Defines A Zone Where All Body And Lead Irregularities Are Allowed.

